

Simultaneous Series

High Performance, Isolated Simultaneous USB Data Acquisition Modules

DT9832, DT9832A, DT9836

Type: Simultaneous Series Bus: USB

DT9832 Series:

- 4 simultaneously captured analog input channels with 16-bit resolution @ 1.25MHz per channel.

DT9832A Series:

- 2 simultaneously captured analog input channels with 16-bit resolution @ 2.0MHz per channel.

DT9836 Series:

- 6 or 12 simultaneously captured analog input channels with 16-bit resolution @ 225kHz per channel.

In addition to the simultaneous channels, all modules feature up to four deglitched waveform analog outputs, 32 digital I/O lines, two 32-bit user counter/timers, and three quadrature decoders. All functions can be synchronously triggered on the same clock.



Figure 1. The BNC connection box packages the OEM embedded versions of the hardware in a CE-compliant enclosure.

Simultaneous USB Series

Model	Simultaneous A/D Throughput per Channel	Highest Signal Frequency for Measurement	Signal Bandwidth	D/A Channels @ 500 kHz	Quad Decoders / Counter Timers	32 High Speed DIO Lines	Applications
DT9832	4 ch @ 1.25MHz	625kHz	>6.25MHz	0 or 2	3 / 2	1.25MHz	Semiconductor Device, Characterization, Nanotechnology Testing, Scientific Analysis, Drug Discovery, Automotive Testing
DT9832A	2 ch @ 2.0MHz	1.0MHz	>10.0MHz	0 or 2	3 / 2	2.0MHz	
DT9836	6 or 12 ch @ 225kHz	112.5kHz	>1.125MHz	0, 2, or 4	3 / 2	225kHz	

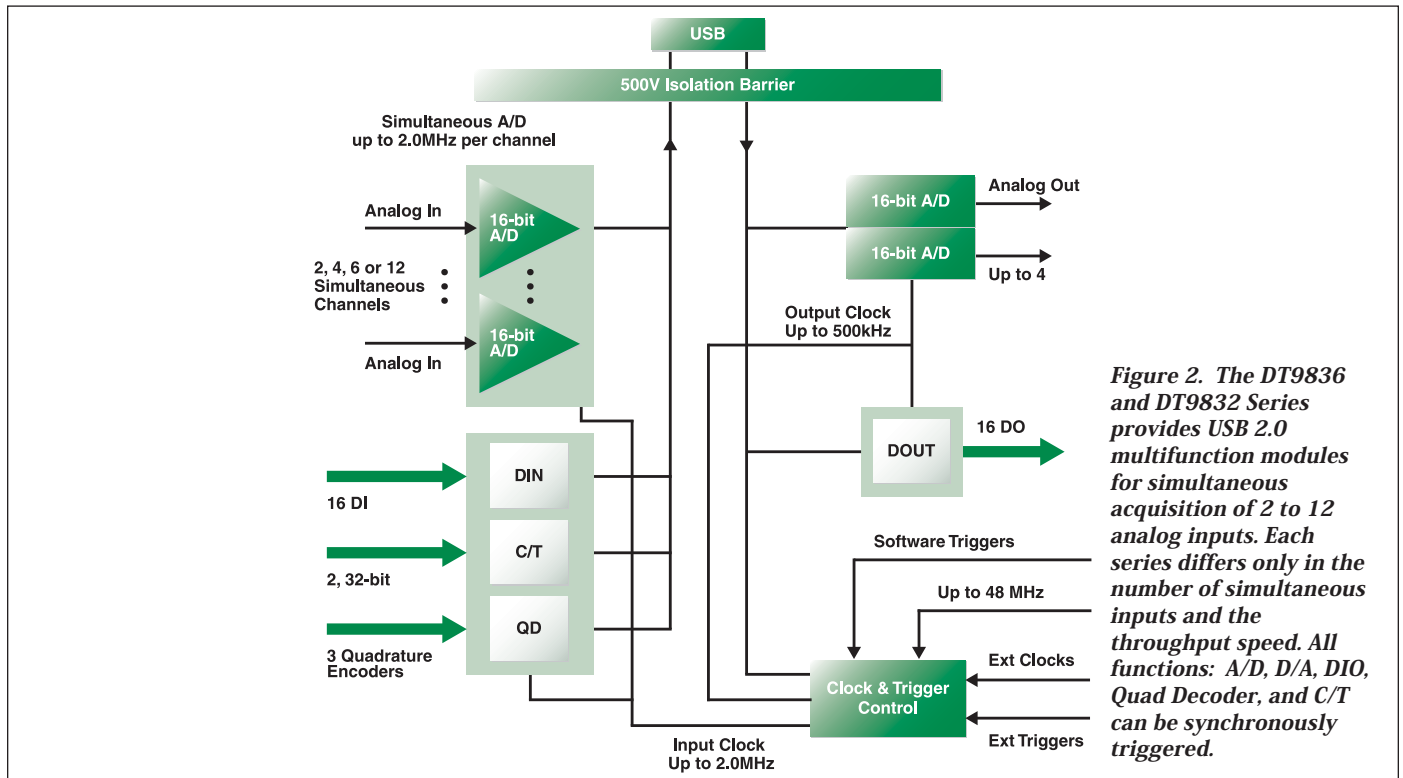


Figure 2. The DT9836 and DT9832 Series provides USB 2.0 multifunction modules for simultaneous acquisition of 2 to 12 analog inputs. Each series differs only in the number of simultaneous inputs and the throughput speed. All functions: A/D, D/A, DIO, Quad Decoder, and C/T can be synchronously triggered.

Simultaneous, High Accuracy Performance

The High Performance Simultaneous Series provides simultaneous analog input operation. Each analog input has its own A/D converter to eliminate phase shift between channels – a problem with multiplexed architectures where all inputs share one A/D converter. As a result, this Series allows you to correlate simultaneous measurements.

Precision Measurements...

True 16-bit A/D at up to 2.0MHz throughput per channel for measuring multiple input signals simultaneously

Designed for Low Noise...

12-layer PCB provides optimal grounding and shielding to maintain signal integrity

No Limits...

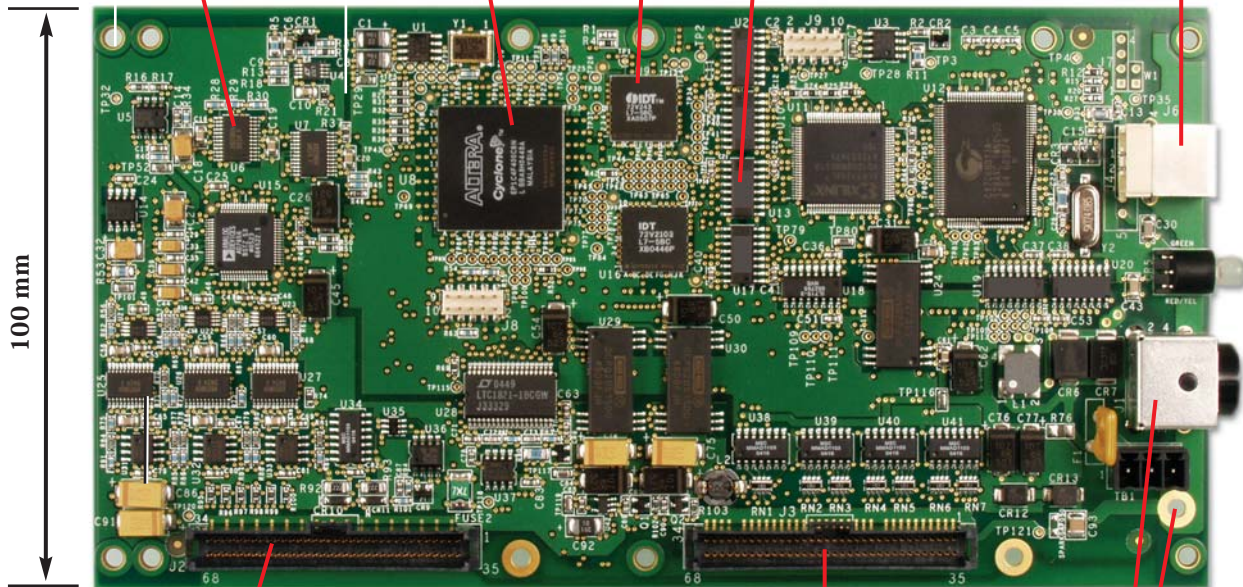
Full simultaneous and synchronous operation of all subsystems

Fully Protected...

500V galvanic isolation protects your computer and maintains signal integrity

High-Speed USB 2.0...

USB 2.0 connector for data transfer at up to 480Mbps



Simultaneous Analog Inputs...

2, 4, 6 or 12 simultaneously sampled analog input channels

Ultra Digital I/O...

Full digital I/O flexibility for time stamping, pattern recognition, and synchronizing with external events

Three Quadrature Decoders...

For X/Y positioning and rotation (tachos)

External Control...

Flexible clocks and triggers

Full-Featured Counter/Timers...

Two 32-bit counter/timers ideal for testing applications.

Flexible Power Connections...

+5 V connector; a secondary +5 V connector is provided for embedded applications

Selection Criteria: Nyquist and Bandwidth Limits Determine Model Choices

Each model has been designed to accurately measure higher bandwidth signal components. To accurately measure 16-bit accuracy, the front-end input amplifier has a bandwidth of ten times the Nyquist limit. Below are examples of these design characteristics for each board.

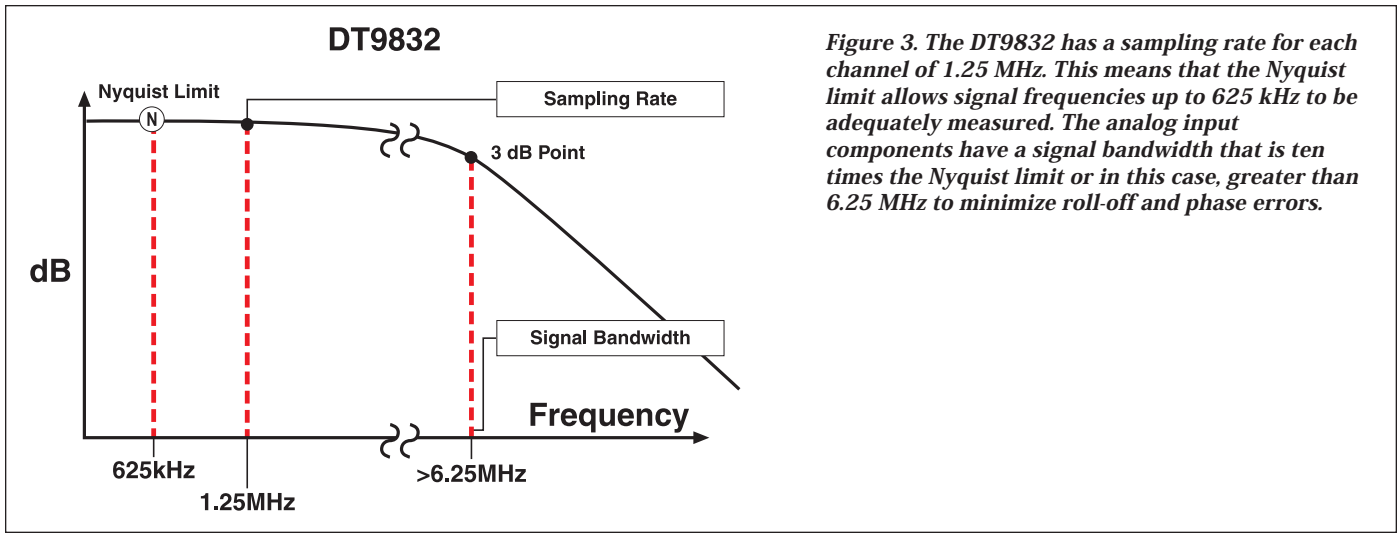


Figure 3. The DT9832 has a sampling rate for each channel of 1.25 MHz. This means that the Nyquist limit allows signal frequencies up to 625 kHz to be adequately measured. The analog input components have a signal bandwidth that is ten times the Nyquist limit or in this case, greater than 6.25 MHz to minimize roll-off and phase errors.

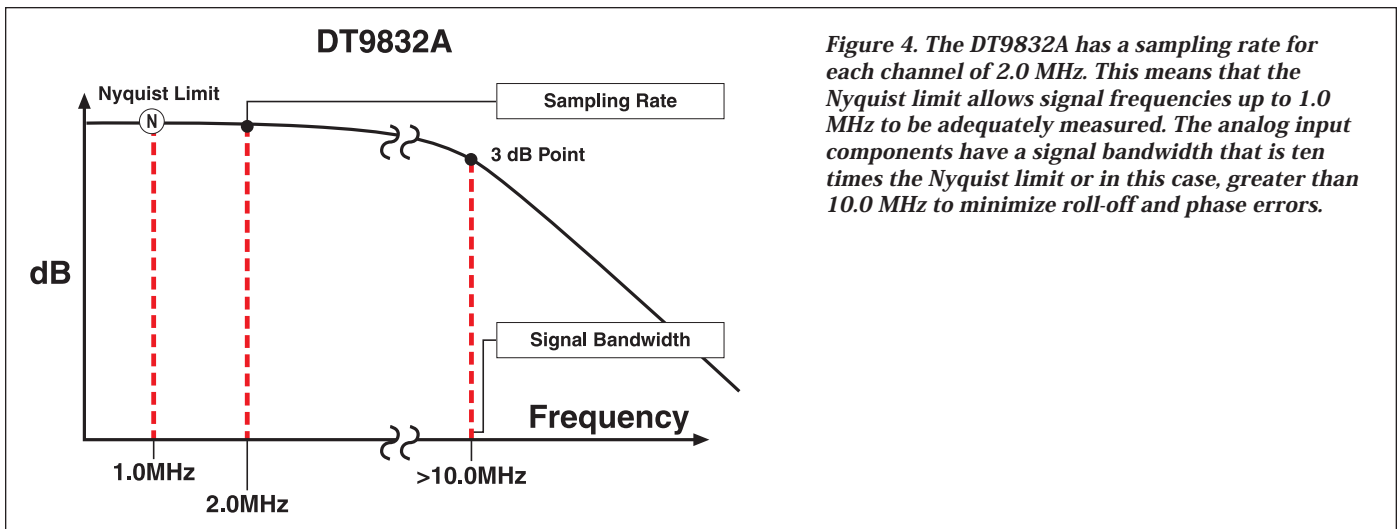


Figure 4. The DT9832A has a sampling rate for each channel of 2.0 MHz. This means that the Nyquist limit allows signal frequencies up to 1.0 MHz to be adequately measured. The analog input components have a signal bandwidth that is ten times the Nyquist limit or in this case, greater than 10.0 MHz to minimize roll-off and phase errors.

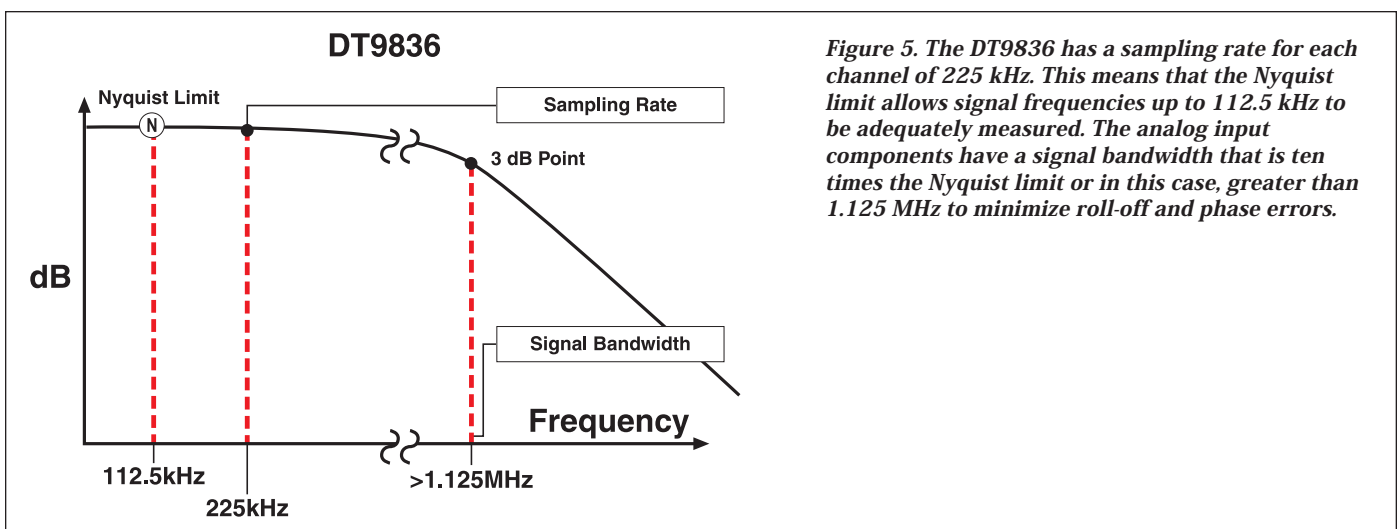


Figure 5. The DT9836 has a sampling rate for each channel of 225 kHz. This means that the Nyquist limit allows signal frequencies up to 112.5 kHz to be adequately measured. The analog input components have a signal bandwidth that is ten times the Nyquist limit or in this case, greater than 1.125 MHz to minimize roll-off and phase errors.

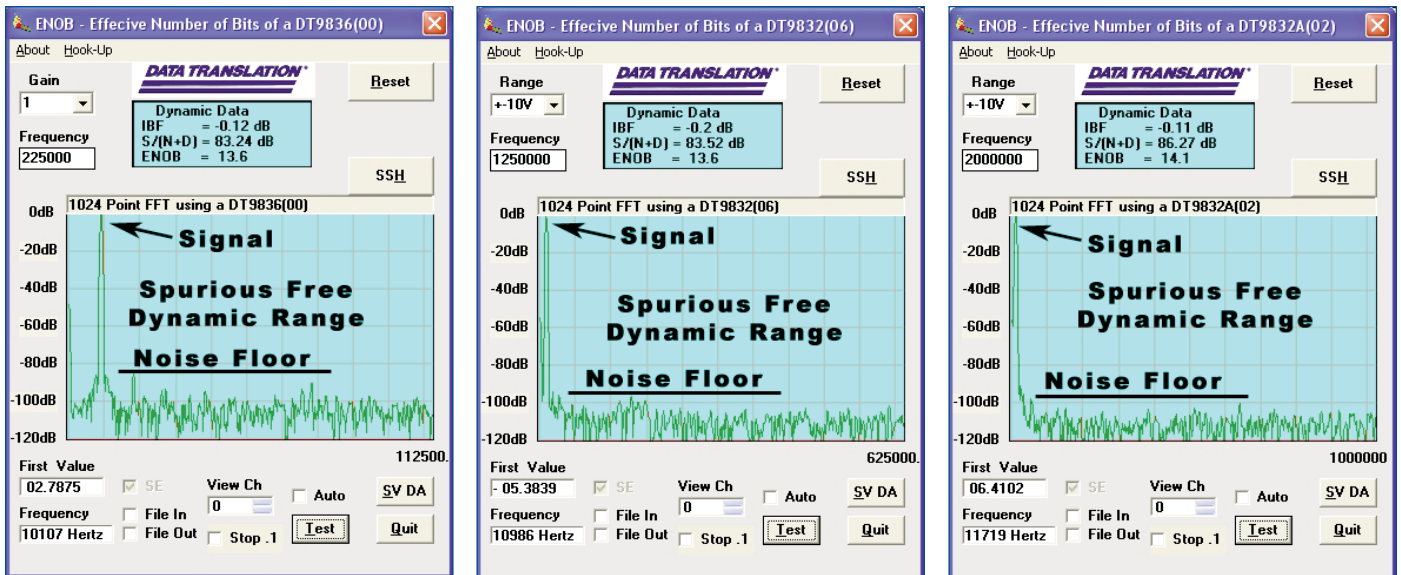


Figure 6. These graphs show the outstanding quality of the DT9836, DT9832, and DT9832A for all error sources... with ENOB (Effective Number Of Bits) ratings of 13.6, 13.6, and 14.1 bits respectively and an SFDR (Spurious Free Dynamic Range) of 86dB.

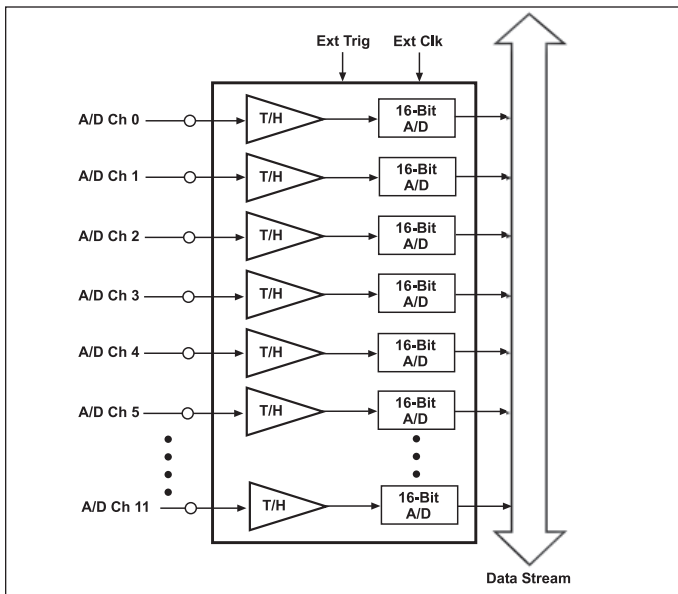


Figure 7. The DT9836 Series features 6 or 12 independent, successive-approximation A/D converters with track-and-hold circuitry. Each converter uses a common clock and trigger for simultaneous sampling of all analog inputs at up to 225kS/s per channel. The DT9832 Series features 2 or 4 simultaneous A/D converters with sampling rates up to 2.0 MHz.

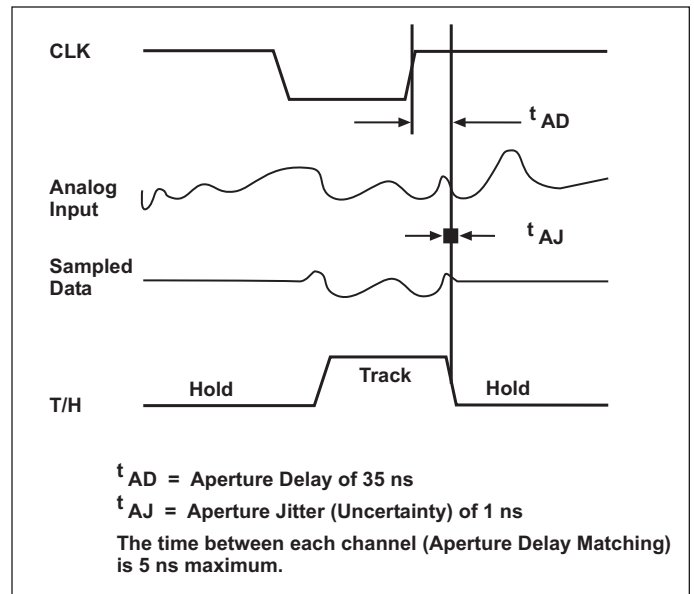


Figure 8. The Simultaneous Series A/D design features built-in accuracy. A maximum aperture delay of 35ns (the time that it takes the A/D to switch from track to hold mode) is well matched at 5ns across all track-and-hold circuits, virtually eliminating the channel-to-channel skew that is associated with multiplexed inputs. A maximum aperture uncertainty of 1ns (the jitter or variance in aperture delay), virtually eliminates phase noise in data.

Overview

The High Performance Simultaneous Series features simultaneous analog inputs, deglitched waveform analog outputs, 32 digital input/output lines, 2 counter/timers, and 3 quadrature decoders. All these subsystems can be run synchronously.

Synchronous Operation

All functions of the data acquisition modules (All A/D, D/A, DIO, Counter Timers, and Quadrature Decoders) can be simultaneously triggered internally or externally. The data can then be streamed synchronously to host memory. This can be done via external trigger or by the internal clock of the module. The synchronous operation allows all I/O data to be processed and correlated for all inputs and outputs. This is very valuable in determining the response across a device-under-test (DUT) to stimuli at the same exact instant.

Simultaneous, High-Resolution Analog Inputs

This Series of modules is available in various analog input channel configurations:

DT9836 Series:

- 12-channel or 6-channel.
- Each analog input signal has its own analog-to-digital converter with sampling rates of up to 225kHz and 16-bit resolution.

DT9832 Series:

- 4-channel.
- Each analog input signal has its own analog-to-digital converter with sampling rates of up to 1.25MHz and 16-bit resolution.

DT9832A Series:

- 2-channel.
- Each analog input signal has its own analog-to-digital converter with sampling rates of up to 2.0MHz and 16-bit resolution.

Closely Matched Analog Inputs

The isolated analog inputs of the simultaneous series have been designed to match each other with high precision. Each input has its own separate high impedance 16-bit A/D converter. The impedance has been carefully matched for each of the inputs so that one looks exactly like the other. The data acquisition board has 12-layers in its make-up to adequately shield and protect each signal path etch from the high speed transitions of the digital lines. The slightest mismatch would result in DC and AC errors in measurement when trying to correlate readings at the same instant in time.

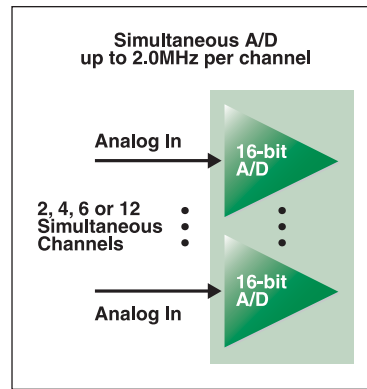


Figure 9. Separate high-speed A/D converters for each input offer parallel signal acquisition with high accuracy.

The SFDR plot in Figure 6 gives the best indication of the match of these DC and AC characteristics. The AC dynamic performance at high switching speeds for all channels shows overall accuracy to be better than 13.6 bits. This is worst case with all errors shown in the FFT ENOB plot. This performance is beyond any other data acquisition system available.

High-Speed, High-Resolution Analog Outputs

There are 2 simultaneous 16-bit analog outputs and an option for 4 on the DT9836, comprising separate high speed, deglitched, waveform D/A converters. This design allows highly accurate arbitrary waveforms to be generated at throughput speeds of 500kHz each. Standard waveforms such as sine, triangle, and square waves are easily produced by loading the output memory and triggering them synchronously or separately. Great care has been used in design to minimize the glitch energy for any major or minor carry. This results in extremely smooth waveforms.

You can update the analog output channels as you are acquiring analog input data for gap-free simultaneous stimulus and response. In addition, you can update the digital output lines with the analog output channels at the analog output rate.

Flexible Output Modes

Using the Simultaneous Series, you can output a single value from a single analog output channel or multiple values from multiple analog output channels. An output channel list gives you the flexibility of updating only the analog output channels you want or updating the digital output lines with specified analog output channels at the D/A clock rate. You can update analog output channels at up to 500 kSamples/s.

Both Series feature the following output modes:

- Continuous output mode – Choose this mode if you want to accurately control the period between conversions of individual output channels in the output channel list.

- **Waveform mode** – Use this mode if you want to output waveforms repetitively from an output FIFO on the module, minimizing communication overhead with the host computer. If you specify only one channel in the output-channel list, you can load a waveform containing up to 128 kSamples into the output FIFO. If you specify all the analog output channels and the digital output lines in the output-channel list, you can load a waveform containing up to 24 kSamples into the output FIFO. Using waveform mode, you can update multiple channels at up to 500 kSamples/s.

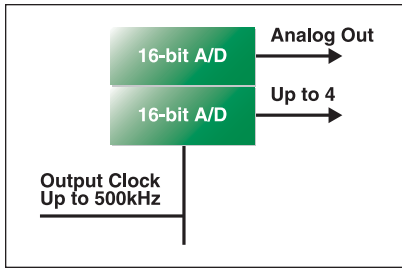


Figure 10. Two or four deglitched, 16-bit D/A converters for pure waveform generation.

High-Speed Digital I/O Lines

The Simultaneous Series modules feature 32 digital I/O lines dedicated as 16 in or 16 out. The first eight digital input lines can also be used for interrupt on change. You can read all the digital input lines simultaneously with the analog input channels at the A/D clock rate. The digital input lines can also be clocked separately as the only channel in the channel-gain list at up to 225 kHz on the DT9836, 1.25 MHz on the DT9832, and 2.0 MHz on the DT9832A.

For digital output operations, you can update all the digital output lines with the analog output channels at the D/A output clock rate.

All lines are EMI protected to minimize interference from transient signals.

Multifunction Counter/Timers

All Simultaneous Series modules feature two 32-bit user counter/timers. If you wish, you can read the value of the counter/timer channels with the analog input channels and digital input lines at the A/D clock rate. The following counter/timer functions are supported: event counting, frequency measurement, pulse width measurement, and period measurement.

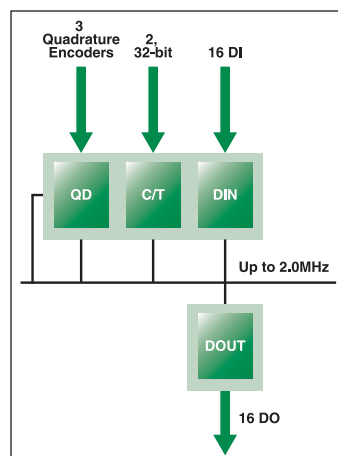


Figure 11. All digital functions can triggered or clocked synchronously along with the analog I/O functions.

Quadrature Decoder

The Quadrature Decoder module contains three quadrature decoders which allow simultaneous decoding of three quadrature encoded inputs. The quadrature decoders may be used to provide relative or absolute position or, by calculating the difference between samples, the rotational speed. Each quadrature decoder supports 'A', 'B', and 'Index' inputs. The index input may be used to zero out the positional count and the A and B input relationships are used to increment or decrement the positional count.

Each decoder features a digital input filter that is programmable from 27ns to 7µs for the DT9836 and from 20ns to 5µs for the DT9832 and DT9832A. This unique filtering capability helps remove ringing edges and unwanted noise.

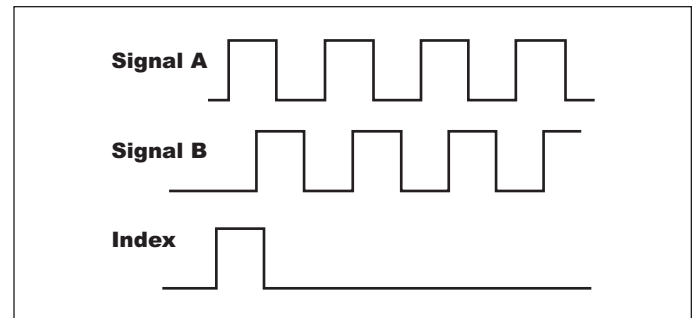


Figure 12. A quadrature decoder takes the output signals (A, B, and Index) for the quadrature encoder as inputs and converts these signals into a numerical value that can be used to determine position, distance, velocity, and other functions.

Flexible Clocks and Triggers

For maximum flexibility, all Simultaneous Series modules provide independent clocks and triggers for the A/D and D/A subsystems. This allows you to trigger and clock the analog output subsystem synchronously with, or independent of, the analog input subsystem. Each subsystem supports an internal clock and external clock input, as well as the following trigger types: software command, analog threshold, and external digital input trigger.

Power

The BNC connection box option includes a separate +5V power supply and power cable for quick setup. OEMs can purchase these options separately as EP361. A secondary power connector is also provided for OEMs to allow custom power wiring.

USB 2.0 Compatibility

These modules are fully compatible with USB 2.0 and USB 1.1. USB 2.0 extends the speed of connection to up to 480 Mbps. For optimal performance, it is recommended that you use the series with a USB 2.0 port. They can be used with a USB 1.1 port, but at USB 1.1 performance.

500V Galvanic Isolation Protects Your Data

Computers are susceptible to ground-spikes through any external port. These spikes can cause system crashes and may even cause permanent damage to your computer. These modules feature 500 Volts of galvanic isolation to protect your computer from ground-spikes and to ensure a reliable stream of data.

EMI and ESD Design Criteria

The simultaneous series has been designed to perform with the lowest noise characteristics. Damping resistors in series with every I/O line minimize ringing and EMI and provide current limits that protect against transient signals.

Flexible Packaging Configurations

The Simultaneous Series modules are available in two packaging configurations: a BNC connection box and an OEM embedded version. The BNC configurations are enclosed in metal boxes with standard BNC and DSUB connectors, 2 BNCs for connecting analog outputs, and 4 BNCs for connecting external clocks and triggers. The BNC configuration ships with a +5 V galvanically isolated power supply and power cable, USB 2.0 cable, and Data Acquisition OMNI CD.

The OEM configuration, ideal for embedding in test systems, provides all the functionality of the Simultaneous Series in PC-board form. This configuration ships with a USB 2.0 cable and Data Acquisition OMNI CD.

Cross-Series Compatibility

Virtually all Data Translation data acquisition modules are compatible with the DT-Open Layers for .NET Class Library. This means that if your application was developed with one of Data Translation's software products, you can easily upgrade to a new Data Translation board. Little or no reprogramming is needed.

User Manual

Each data acquisition module includes a user's manual that provides getting started and reference information. The manual is provided in electronic (PDF) format on the Data Acquisition Omni CD provided with the module.

Technical Support

Application engineers are available by phone and email during normal business hours to discuss your application requirements. Extensive product information, including drivers, example code, pinouts, a searchable Knowledge Base, and much more, is available 24 hours a day on our web site at www.datatranslation.com.



Figure 13. The High Performance Simultaneous Series modules are available in two configurations: BNC connection box and OEM embedded version. Both have 500V galvanic isolation.

Accessories for OEM Configurations

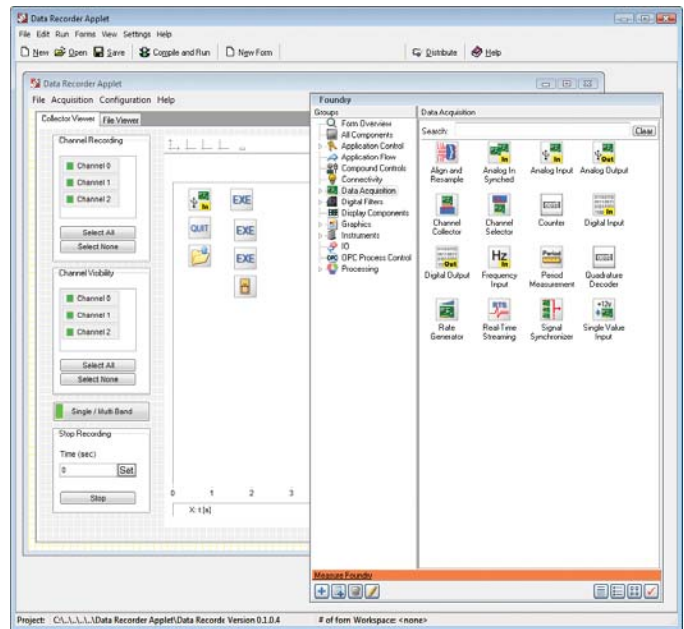
For applications where you want to embed a DT9836 or DT9832/DT9832A Series module inside other equipment, use the OEM packaging configuration (no enclosure) and our optional accessories.

Software Options

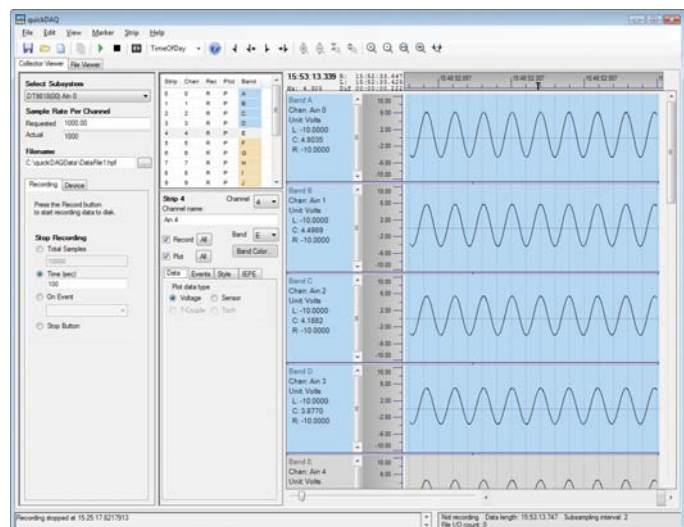
There are many software choices available for application development, from ready-to-measure applications to programming environments.

The following software is available for use with the DT9832 and DT9836 Series modules and is provided on the Data Acquisition Omni CD:

- **Measure Foundry®** – An evaluation version of this software is included on the Data Acquisition Omni CD. Measure Foundry® is a drag-and-drop test and measurement application builder designed to give top performance with ease-of-use development.
- **Measurement Applets** – These small applications, developed with Measure Foundry, can be modified or combined to provide a specific solution. Applets are included with the Measure Foundry evaluation version and are also available for download from our website. Order the full development version of Measure Foundry to develop applications using real hardware.
- **quickDAQ application** – An evaluation version of this .NET application is included on the Data Acquisition Omni CD. quickDAQ acquires analog data from all devices supported by DT-Open Layers for .NET software at high speed, plots it during acquisition, analyzes it, and/or saves it to disk for later analysis.
- **Quick DataAcq application** – The Quick DataAcq application provides a quick way to get up and running using a DT9832 or DT9836 Series module. Using this application, verify key features of the module, display data on the screen, and save data to disk.
- **DT-Open Layers® for .NET Class Library** – Use this class library if you want to use Visual C#® or Visual Basic® for .NET to develop application software for a DT9832 or DT9836 Series module using Visual Studio® 2003/2005/2008; the class library complies with the DT-Open Layers standard.
- **DataAcq SDK** – Use the Data Acq SDK to use Visual Studio 6.0 and Microsoft® C or C++ to develop application software for a DT9832 or DT9836 Series module using Windows®; the DataAcq SDK complies with the DT-Open Layers standard.
- **DTx-EZ** – DTx-EZ provides ActiveX® controls, which allows access to the capabilities of a DT9832 or DT9836 Series module using Microsoft Visual Basic or Visual C++®; DTx-EZ complies with the DT-Open Layers standard.
- **DAQ Adaptor for MATLAB** – Data Translation's DAQ Adaptor provides an interface between the MATLAB® Data Acquisition (DAQ) toolbox from The MathWorks™ and Data Translation's DT-Open Layers architecture.
- **LV-Link** – An evaluation version of this software is included on the Data Acquisition Omni CD. Use LV-Link to use the LabVIEW™ graphical programming language to access the capabilities of the DT9832 or DT9836 Series module.



The data recorder applet is developed with Measure Foundry and allows you to acquire data, plot it, and save it to disk.



quickDAQ acquires analog data from all devices supported by DT-Open Layers for .NET software at high speed, plots it during acquisition, analyzes it, and/or saves it to disk for later analysis.

Ordering Summary

Data Acquisition Modules:

Model	Analog In	Analog Out	Throughput	Digital In / Out	C/T	Q/D	Packaging
DT9832-04-0-BNC	4SE	0	1.25 MHz	16 / 16	2	3	BNC
DT9832-04-0-OEM	4SE	0	1.25 MHz	16 / 16	2	3	OEM
DT9832-04-2-BNC	4SE	2	1.25 MHz	16 / 16	2	3	BNC
DT9832-04-2-OEM	4SE	2	1.25 MHz	16 / 16	2	3	OEM
DT9832A-02-0-BNC	2SE	0	2.0 MHz	16 / 16	2	3	BNC
DT9832A-02-0-OEM	2SE	0	2.0 MHz	16 / 16	2	3	OEM
DT9832A-02-2-BNC	2SE	2	2.0 MHz	16 / 16	2	3	BNC
DT9832A-02-2-OEM	2SE	2	2.0 MHz	16 / 16	2	3	OEM
DT9836-06-0-BNC	6SE	0	225 kHz	16 / 16	2	3	BNC
DT9836-06-0-OEM	6SE	0	225 kHz	16 / 16	2	3	OEM
DT9836-06-2-BNC	6SE	2	225 kHz	16 / 16	2	3	BNC
DT9836-06-2-OEM	6SE	2	225 kHz	16 / 16	2	3	OEM
DT9836-06-4-OEM	6SE	4	225 kHz	16 / 16	2	3	OEM
DT9836-12-0-BNC	12SE	0	225 kHz	16 / 16	2	3	BNC
DT9836-12-0-OEM	12SE	0	225 kHz	16 / 16	2	3	OEM
DT9836-12-2-BNC	12SE	2	225 kHz	16 / 16	2	3	BNC
DT9836-12-2-OEM	12SE	2	225 kHz	16 / 16	2	3	OEM

Packaging Configurations:

OEM – Board-level embedded version for maximum flexibility. *Power supply not included.*

BNC – Metal box enclosure with BNCs for analog inputs. If you select a model with analog outputs, BNCs are provided. The BNC box configuration also provides 4 BNCs for connecting external clocks and triggers. EP361 power supply and power cable included.

Software:

The following software can be purchased separately:

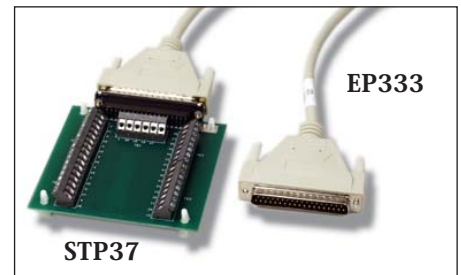
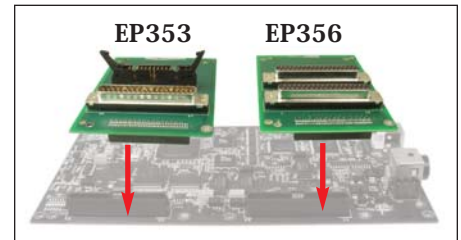
- **Measure Foundry** – Test and measurement application builder for Windows® XP/Vista. SP1300-CD.
- **quickDAQ** – High-performance, ready-to-run application that lets you acquire, plot, analyze, and save data to disk at up to 2 MHz per channel. SP8501-CD
- **LV-Link** – Access the power of Data Translation boards through LabVIEW™.

The following software is available as a free download from our website:

- **DAQ Adaptor for MATLAB** – Access the analyzation and visualization tools of MATLAB®.

Accessories:

- **EP333** – Cable with two 37-pin male DSUB connectors between STP37 and EP356 or BNC box.
- **EP353** – Accessory panel with 1, 37-pin DSUB connector and 1, 26-pin signal conditioning connector for attaching analog input signals (for OEM configurations only).
- **EP355** – Screw terminal panel for attaching analog I/O and digital I/O signals (for OEM configurations only).
- **EP356** – Accessory panel with 2, 37-pin DSUB connectors for attaching analog output, counter/timer, trigger, clock signals, and digital I/O signals (for OEM configurations only).
- **EP360** – Cable with one 37-pin female and one 37-pin male DSUB connector between STP37 and EP353 or BNC box.
- **EP361** – A +5 V power supply (included with BNC configurations).
- **STP37** – 37-pin screw terminal panel that connects to the EP356, EP353, or BNC box via an EP333 or EP360 cable.



All Data Translation hardware products are covered by a 1-year warranty. For pricing information, see the current price list, visit our website, or contact your local reseller.

For more information about the DT9832 Series, please visit:
<http://www.datatranslation.com/go/DT9832/>

For more information about the DT9836 Series, please visit:
<http://www.datatranslation.com/go/DT9836/>